

PTO-1449 REPRODUCED

INFORMATION DISCLOSURE CITATION
IN AN APPLICATION

March 2, 2004

(Use several sheets if necessary)

ATTORNEY DOCKET NO.
2376.1001-003APPLICATION NO.
10/675,248FIRST NAMED INVENTOR
Anastasios S. MaurudisFILING DATE
September 30, 2003EXAMINER
R. Frejd
~~Not assigned~~CONFIRMATION NO.
3497GROUP
~~2123~~ 2128

U.S. PATENT DOCUMENTS

EXAM- INER INI- TIAL	REF. NO.	DOCUMENT NUMBER Number-Kind Code (if known)	ISSUE DATE / PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE OR APPLICANT OF CITED DOCUMENT
RF	AA	4,893,267	01/09/1990	Alsup <i>et al.</i>
	AB	5,295,222	03/15/1994	Wadhwa <i>et al.</i>
	AC	5,583,983	12/10/1996	Schmitter
	AD	4,135,242	01/16/1979	Ward <i>et al.</i>
	AE	5,560,013	09/24/1996	Scalzi <i>et al.</i>
	AF	5,613,098	03/18/1997	Landau <i>et al.</i>
	AG	5,768,593	06/16/1998	Walters <i>et al.</i>
	AH	6,173,247 B1	01/09/2001	Maurudis <i>et al.</i>
V	AI	6,011,872	01/04/2000	Qian <i>et al.</i>
RF	AJ	5,732,005	03/24/1998	Kahle <i>et al.</i>
	AK			
	AA2			
	AB2			
	AC2			
	AD2			
	AE2			
	AF2			
	AG2			

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER Country Code-Number-Kind Code (if known)	DATE MM-DD-YYYY	NAME OF PATENTEE OR APPLICANT OF CITED DOCUMENT	TRANSLATION YES NO	
RP	AL	EP 0 718 757 A2	06/26/1996	Motorola		
	AM					
	AN					

EXAMINER

Russell Frejd

DATE CONSIDERED

05/25/2006

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PTO-1449 REPRODUCED INFORMATION DISCLOSURE CITATION IN AN APPLICATION March 2, 2004 (Use several sheets if necessary)	ATTORNEY DOCKET NO. 2376.1001-003	APPLICATION NO. 10/675,248	
	FIRST NAMED INVENTOR Anastasios S. Maurudis		FILING DATE September 30, 2003
	EXAMINER Not assigned R. Frejd	CONFIRMATION NO. 3497	GROUP 2123 2128

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RF	AR	Maurudis, A.S., "FACT™: A C++ Environment for Accurately Modeling Fixed-Point Digital Signal Processors," presented at conference on Intelligent Methods for Signal Processing and Communications, Universidad de Vigo, Baiona (Vigo), Spain, June 24-26, 1996.
	AS	Maurudis, A.S., "FACT™: A C++ Environment for Accurately Modeling Fixed-Point Digital Signal Processors," <i>The Proceedings of the 7th International Conference on Signal Processing Applications & Technology</i> , Vol. 1, p.846-851, Boston, Mass., U.S.A., October 7-10, 1996.
	AT	Maurudis, A.S., "An Efficient Vector-Space Approach for Accurately Modeling Fixed-Point Digital Signal Processors," <i>1996 IEEE TENCON - Digital Signal Processing Applications</i> , pp. 659-664 (November 27-29, 1996).
	AU	Ombres, D., "C and C++ Extensions Simplify Fixed-Point DSP Programming," <i>EDN</i> , pp. 135-138, October 10, 1996.
	AV	"Digital Signal Processing Solutions Support," http://www.ti.com/sc/docs/dsp/develop/3rdparty/consult/458tarta.htm , (downloaded 6/3/97).
	AW	Harton, M. and K. Kapsucinski, "BEC++" A software tool for increased flexibility in algorithm development," <i>IEEE</i> 0-7803-5651-9/99, pp. 67-69.
	AX	Edwards, C., "Library to model DSP Algorithms," <i>Electronics Times</i> No. 908, p.14, June 1998.
	AY	Robe, E.D. and D. Irwin, "SIMULINK, Modules that Emulate Digital Controllers Realized with Fixed-Point or Floating-Point Arithmetic," <i>IEEE</i> paper; 0-8186-5320-5/94, 1994, pp. 337-341.
	AZ	Kraeling, M.B., "Fixed-Point Math in Time-Critical Applications," <i>IEEE, WESCON/96</i> , October 1996, pages 587-593.
	AR2	Kambi, S.J., <i>et al.</i> , "Error Analysis of Filters Implemented with Floating Point Arithmetic," <i>Proceedings of the 26th Southeastern Symposium on System Theory</i> , <i>IEEE</i> , March 1994, pages 47-51.
RF	AS2	Lee, <i>et al.</i> , "Target Bit Matching for MPEG-2 Video Rate Control," <i>IEEE Region 10 International Conference on Global Connections, Energy, Computer, Communication and Control</i> , December 1998, pages 66-69.

EXAMINER Russell Frejd	DATE CONSIDERED 05/25/2006
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